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Application Number	10/787, 116
Filing Date	Feb. 27, 2004
First Named Inventor	Tadahiko Hisano
Group Art Unit	2182
Examiner Name	Rehana Perveen
Attorney Docket Number	

(use as many sheets as necessary)

Sheet	1	of	1
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[illegible][illegible]

**Examiner
Signature**

Date Considered

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
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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	
		Filing Date	
		First Named Inventor	Tadahiko Hisano
		Group Art Unit	
		Examiner Name	
Sheet	2	of	2
		Attorney Docket Number	

FOREIGN PATENT DOCUMENTS							
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	
		Office ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	A-60-215267		Kong K K	10/28/1985	
		JP	A-63-110947		NEC Corp.	07/16/1988	
		JP	A-6-149431		Tokyo Electric Co., Ltd	05/27/1994	
		WO	WO 93/23811		SOUTHWESTER BELL TEC.	11/25/1993	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		80186/80188 Intel User's Manual (Nov., 1994).	
		P. Oguic, "Carte E/S externe pour port parallel", Electronique Radio Plans, No. 567, Feb. 1, 1994, pp. 43-47.	
		B. J. Freeman et al., : "Microprocessor-to-microprocessor communications via an 8-bit data bus" IBM Technical Disclosure Bulletin., vol. 25, No. 10, Mar. 1983, pp. 5230-5235.	

Examiner Signature	Date Considered 6/22/04
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